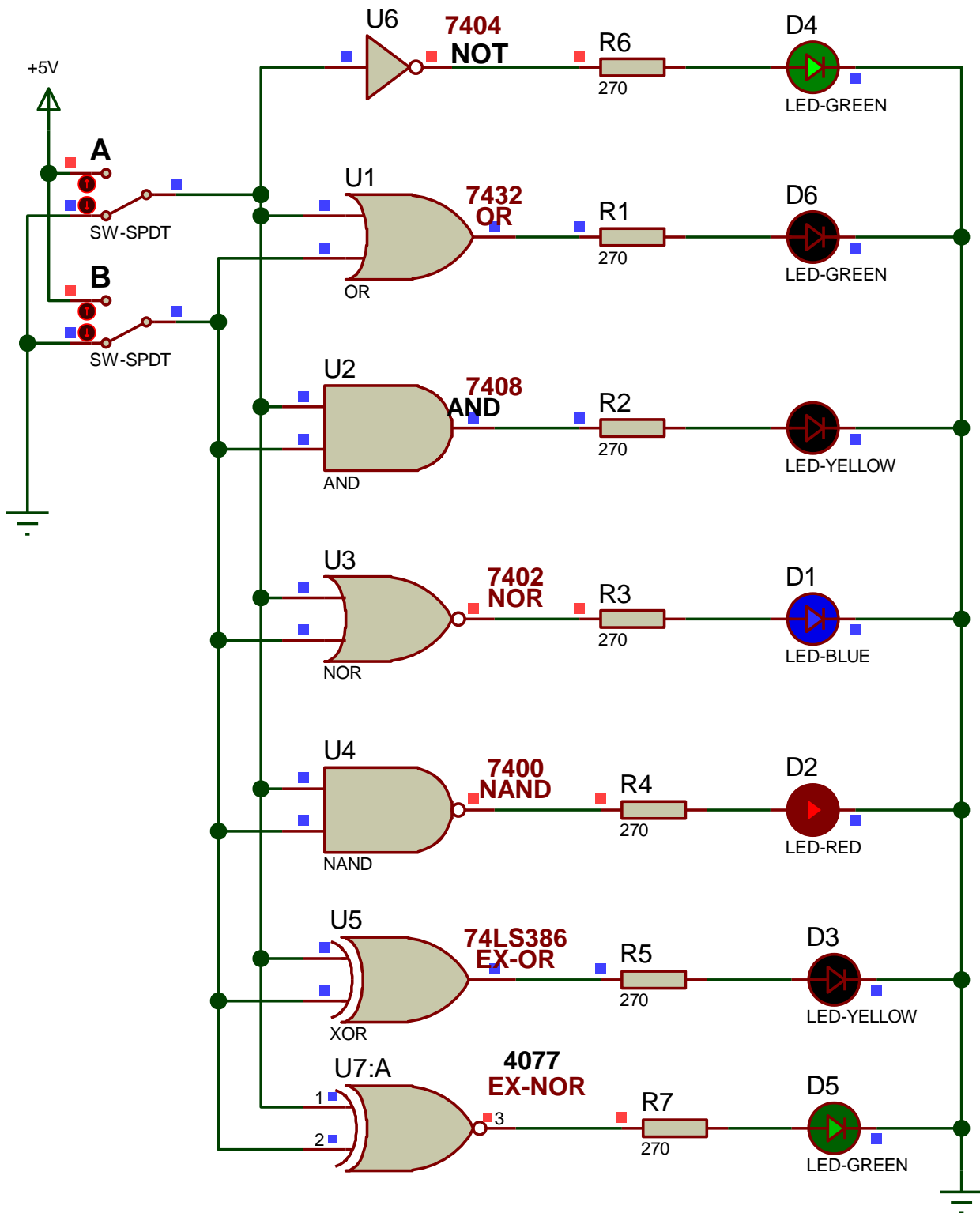
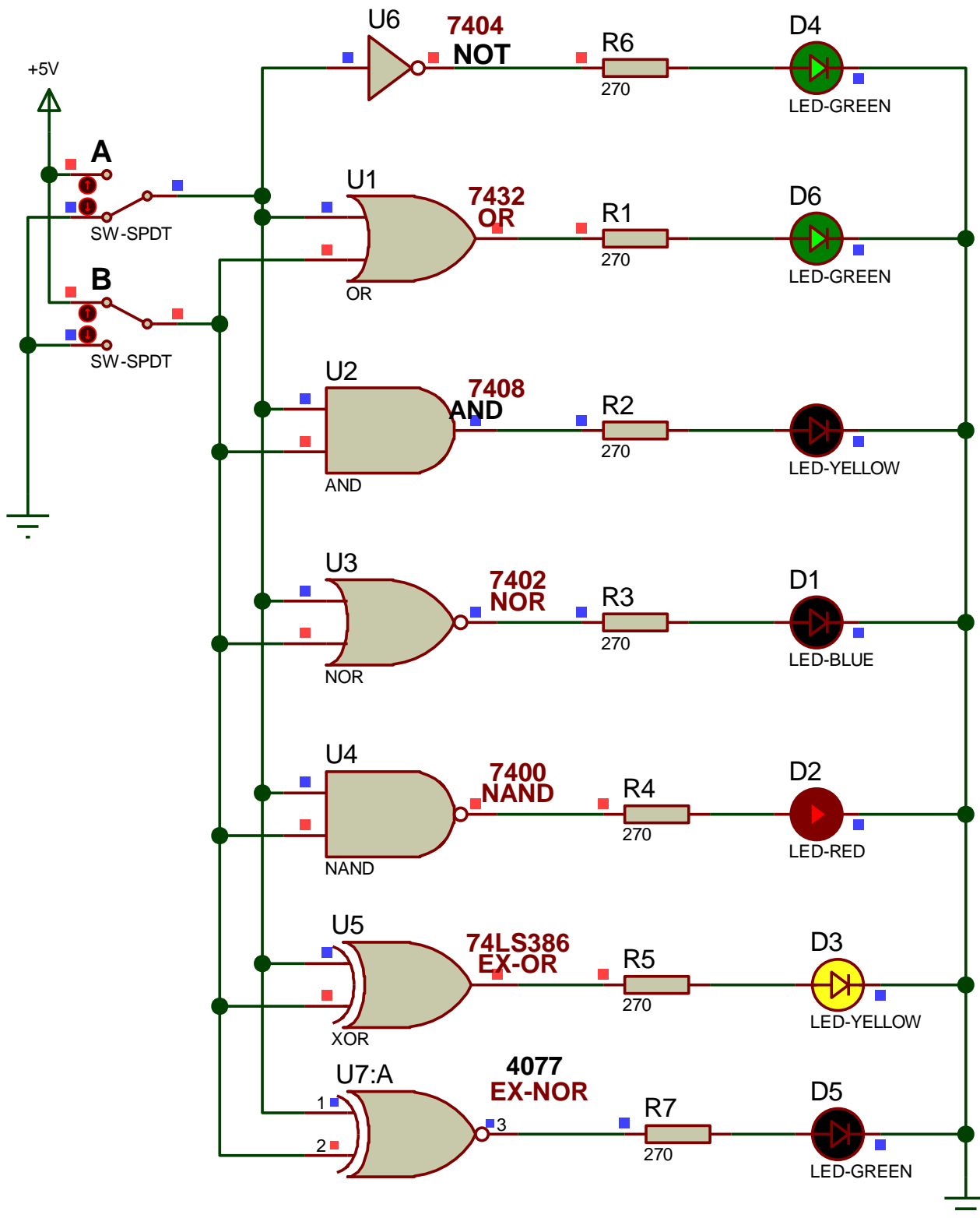


1. VERIFICA TAVOLE DI VERITA' PORTE LOGICHE ELEMENTARI

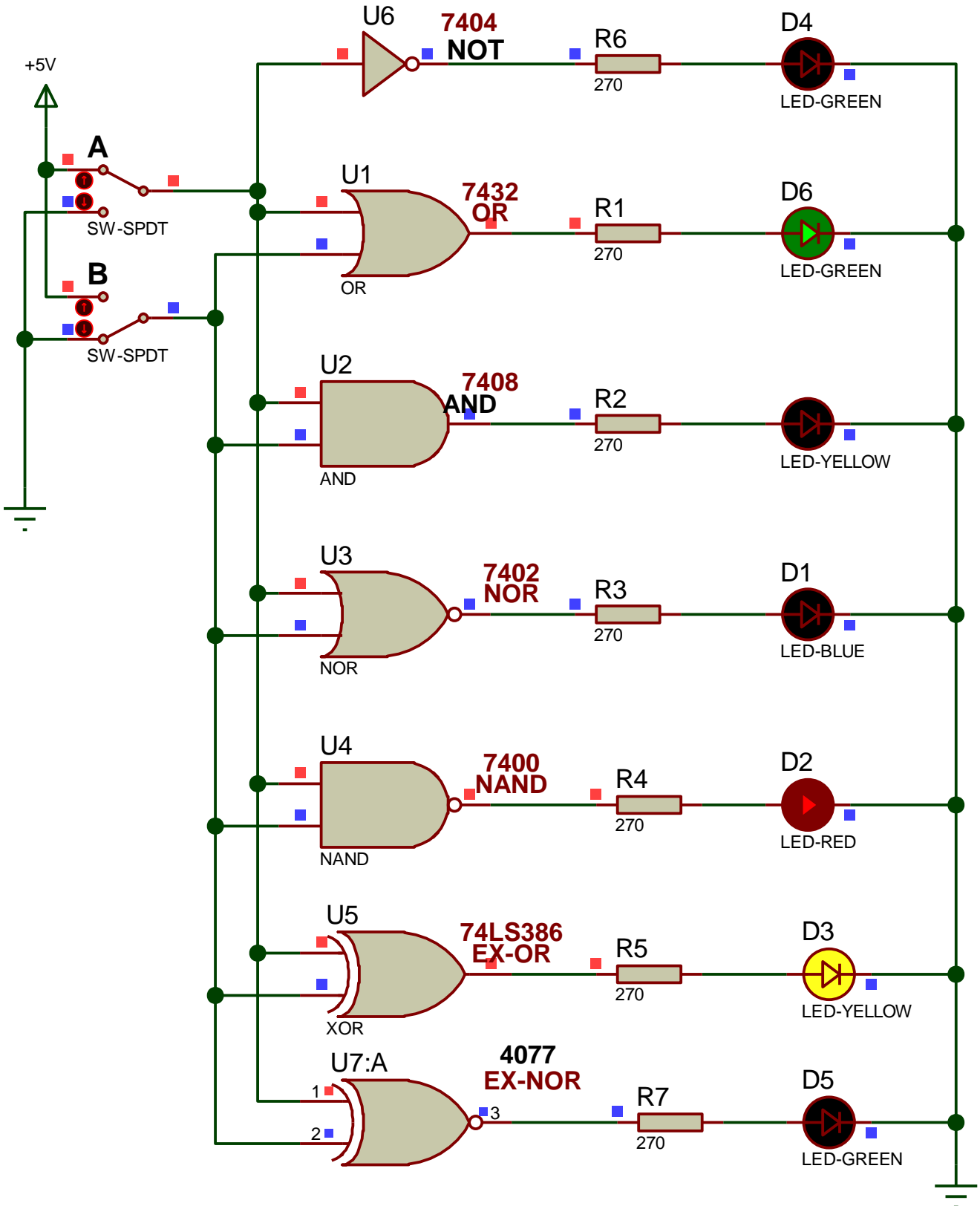
1° COMANDO : **A=0 , B=0** >>>> $V_a = 0 [V]$; $V_b = 0 [V]$



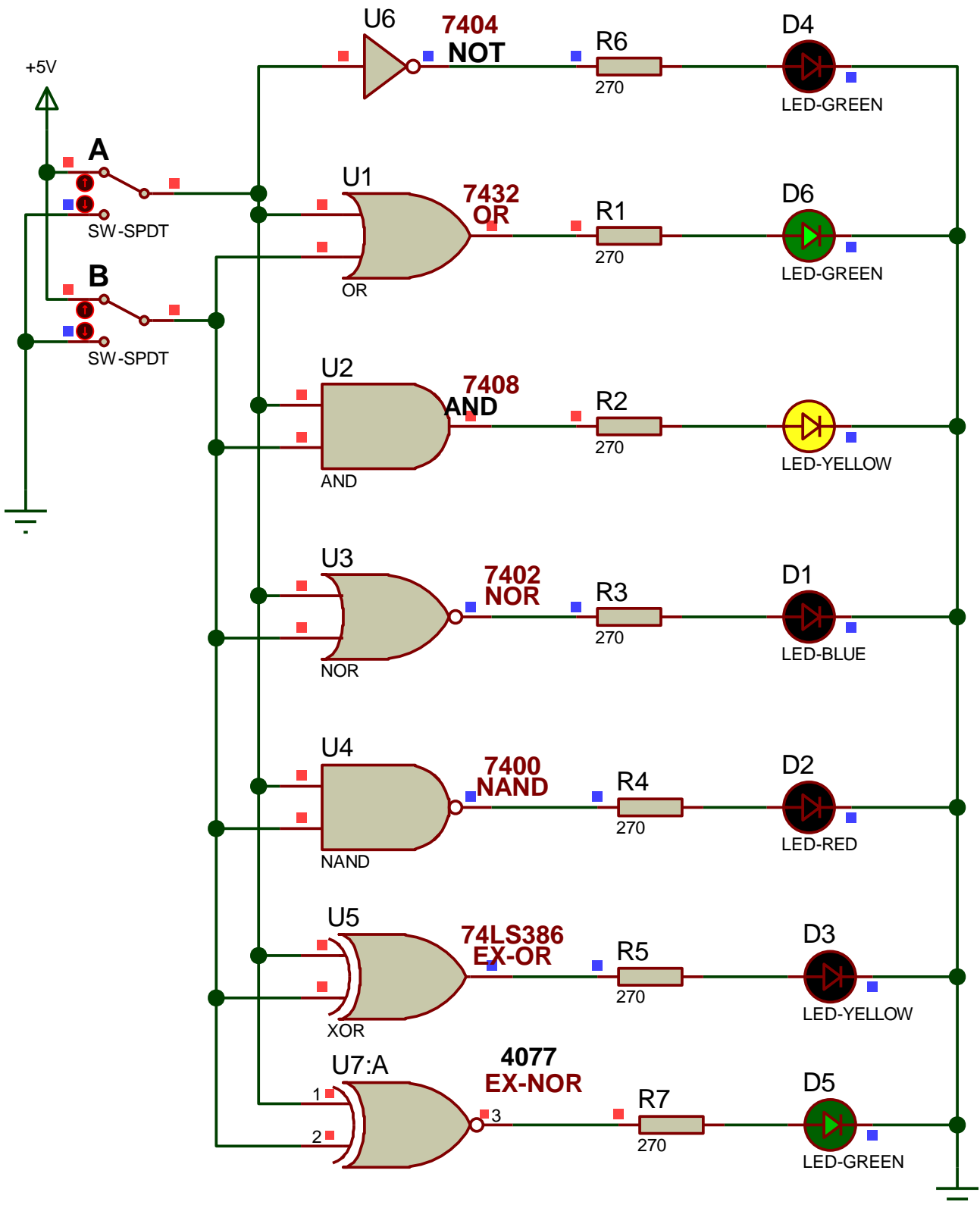
2° COMANDO : **A=0 , B=1** >>>> $V_a = 0 [V]$; $V_b = 5 [V]$



3° COMANDO : **A=1 , B=0** >>>> $V_a = 5 [V]$; $V_b = 0 [V]$



4° COMANDO : **A=1 , B=1** >>>> $V_a = 5 [V]$; $V_b = 5 [V]$



2. TAVOLE DI VERITA' PORTE LOGICHE

1. NOT : CIRCUITO INTEGRATO TTL 7404

A	$Y = \bar{A}$
0	1
1	0

2. OR : CIRCUITO INTEGRATO TTL 7432

A	B	$Y = A+B$
0	0	0
0	1	1
1	0	1
1	1	1

3. AND : CIRCUITO INTEGRATO TTL 7408

A	B	$Y = A*B$
0	0	0
0	1	0
1	0	0
1	1	1

4. NOR : CIRCUITO INTEGRATO TTL 7402

A	B	$Y = \overline{A+B}$
0	0	1
0	1	0
1	0	0
1	1	0

5. NAND : CIRCUITO INTEGRATO TTL 7400

A	B	$Y = \overline{A*B}$
0	0	1
0	1	1
1	0	1
1	1	0

6. EX-OR : CIRCUITO INTEGRATO TTL 74LS386

A	B	$Y = A \text{ EX-OR } B$
0	0	0
0	1	1
1	0	1
1	1	0

7. EX-NOR : CIRCUITO INTEGRATO C-MOS 4077

A	B	$Y = A \text{ EX-NOR } B$
0	0	1
0	1	0
1	0	0
1	1	1

3. FUNZIONE LOGICA A 3 VARIABILI

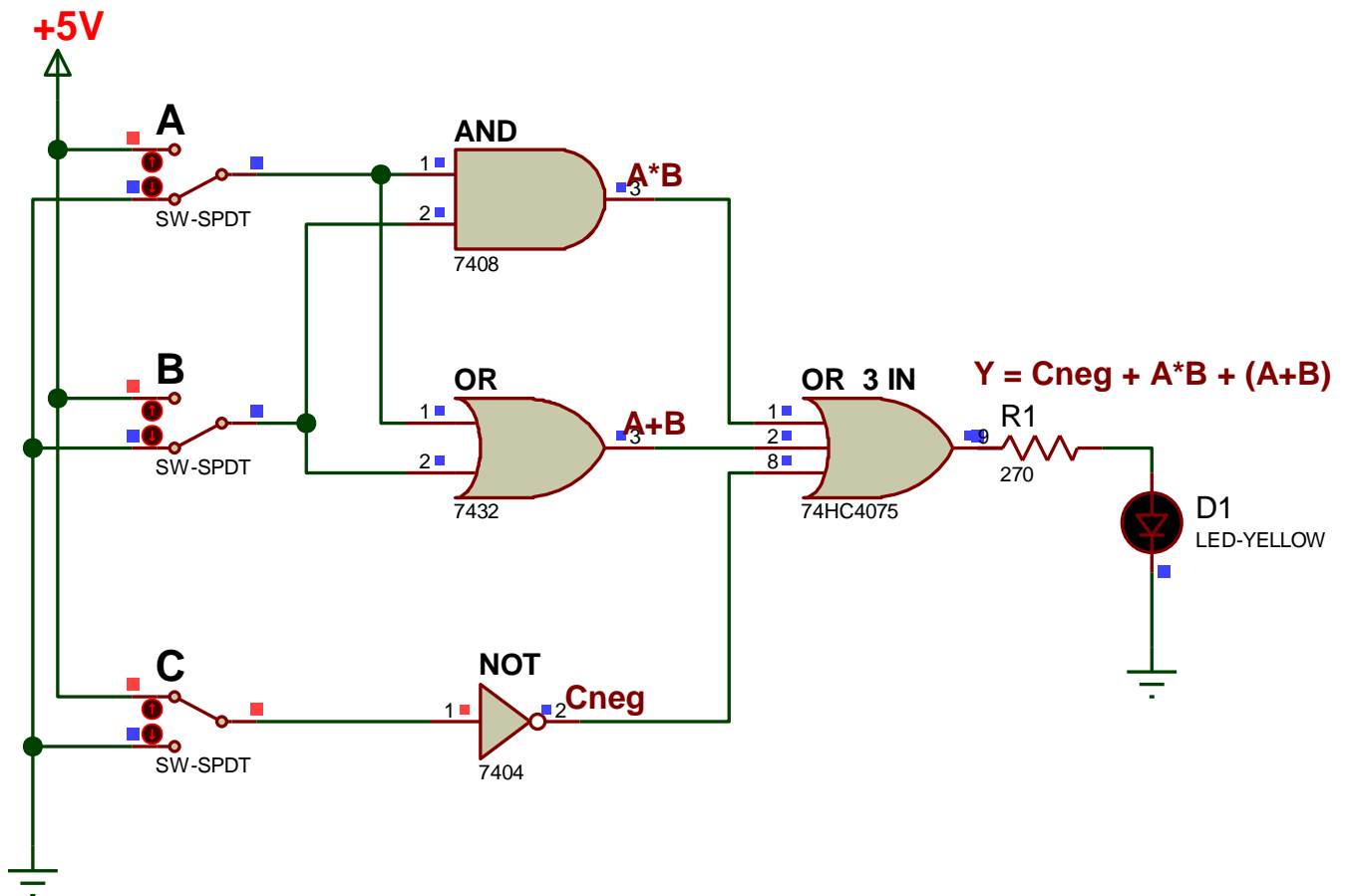
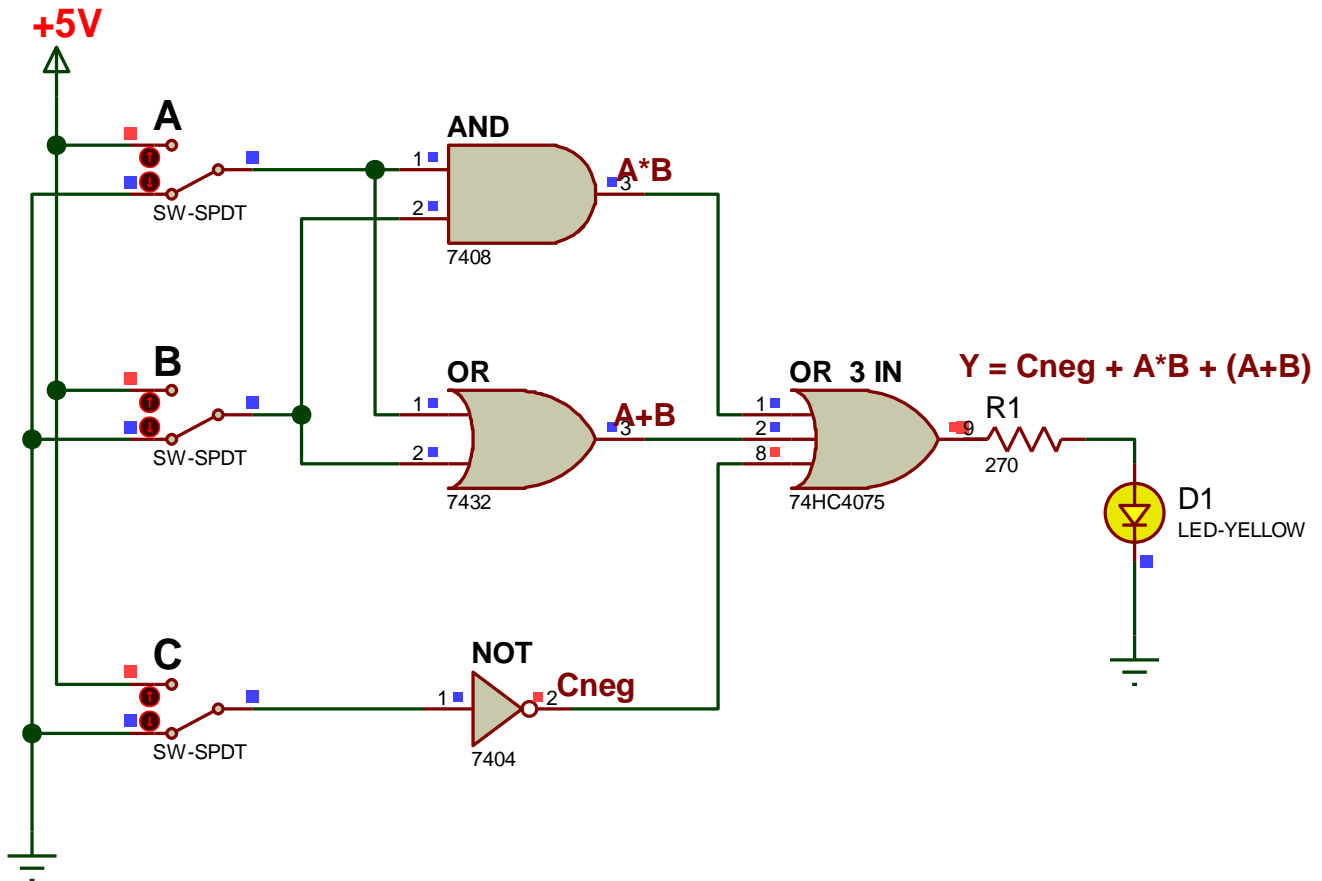
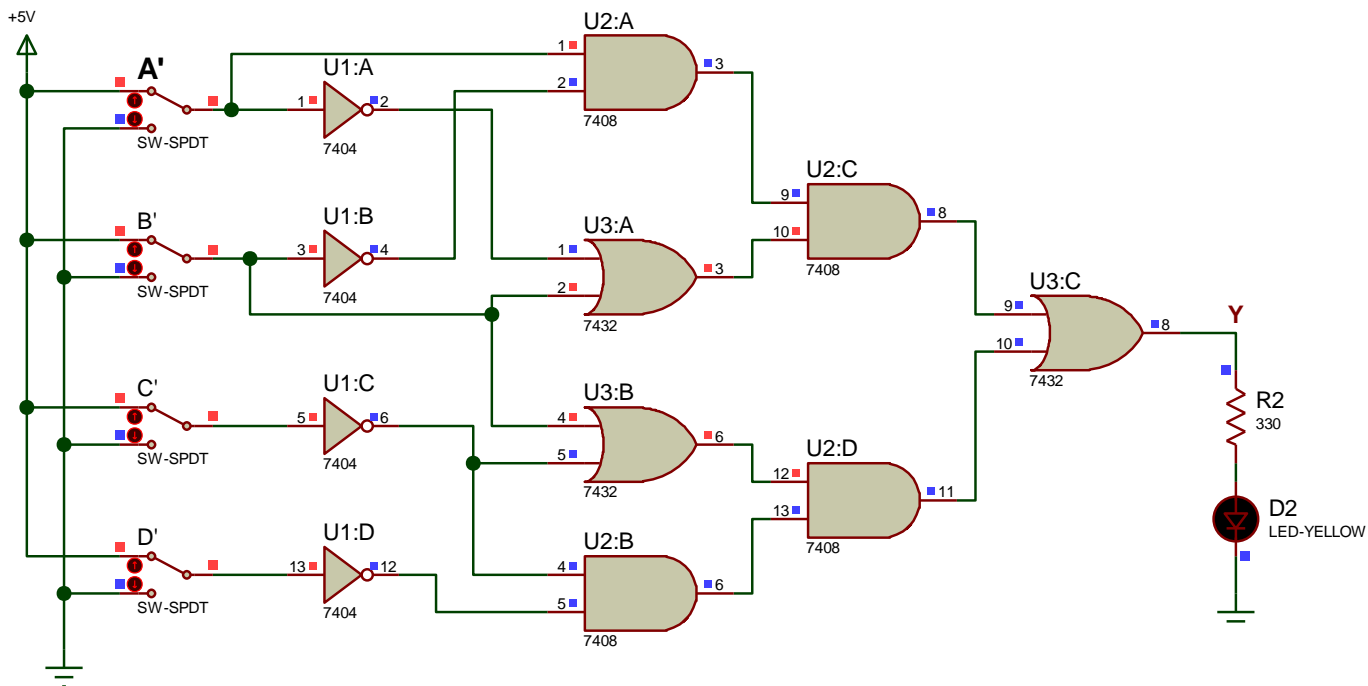
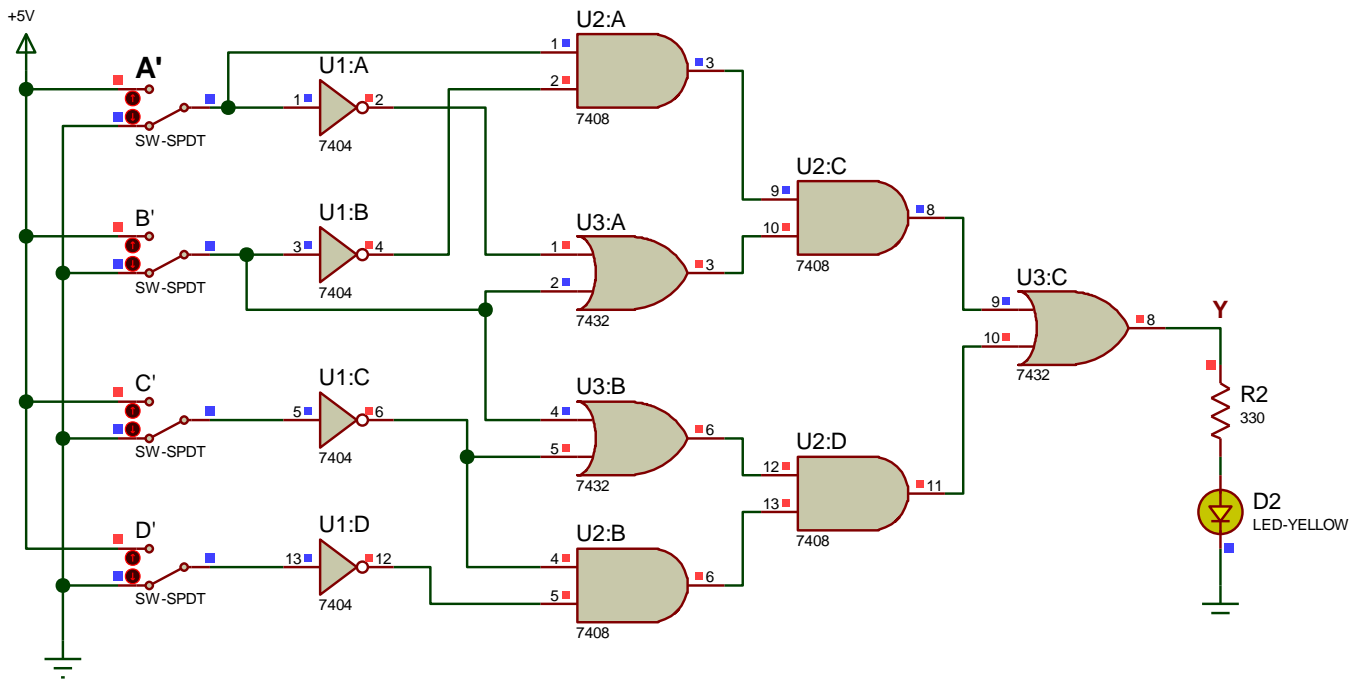


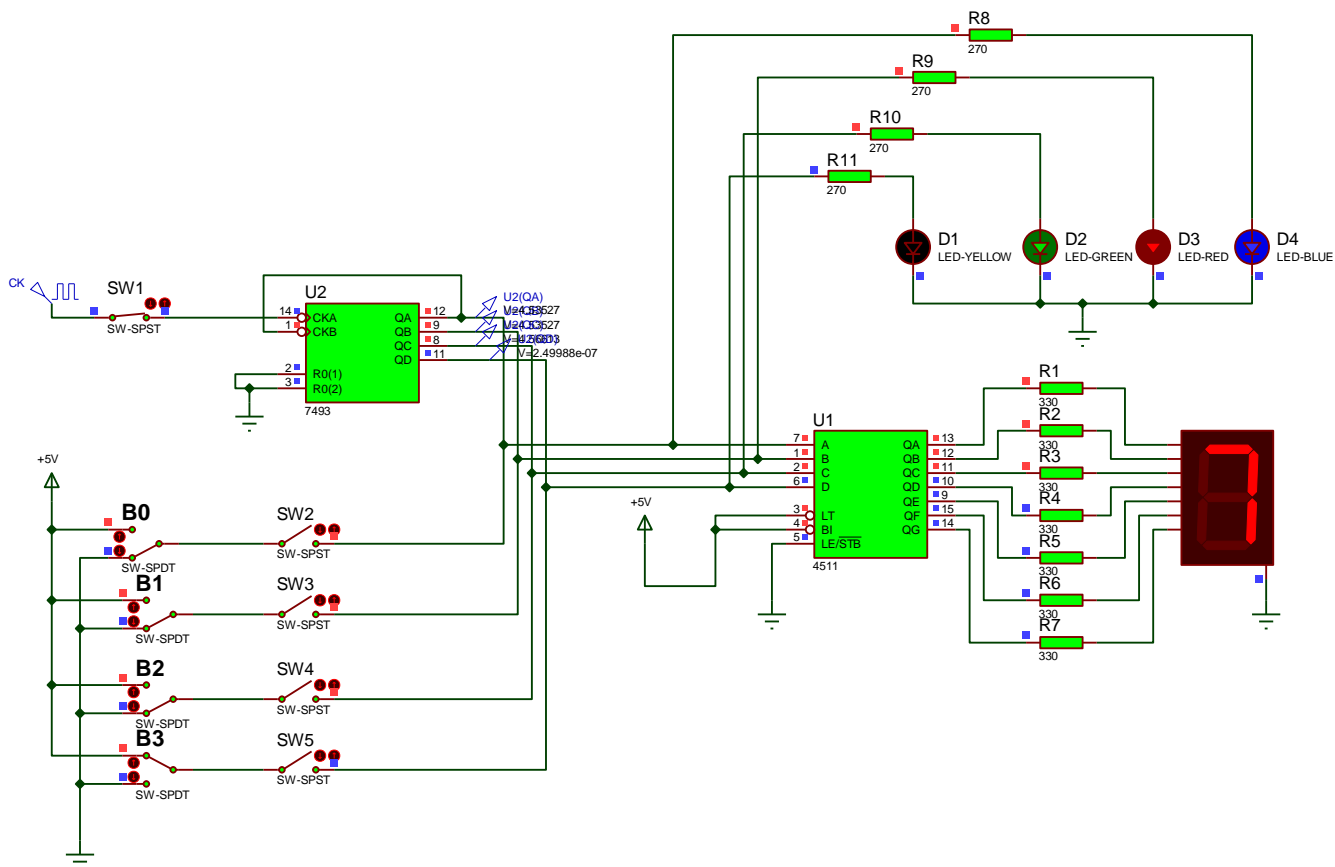
TAVOLA DI VERITA'

A	B	C	$Y=A*B + (A+B) + \bar{C}$
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

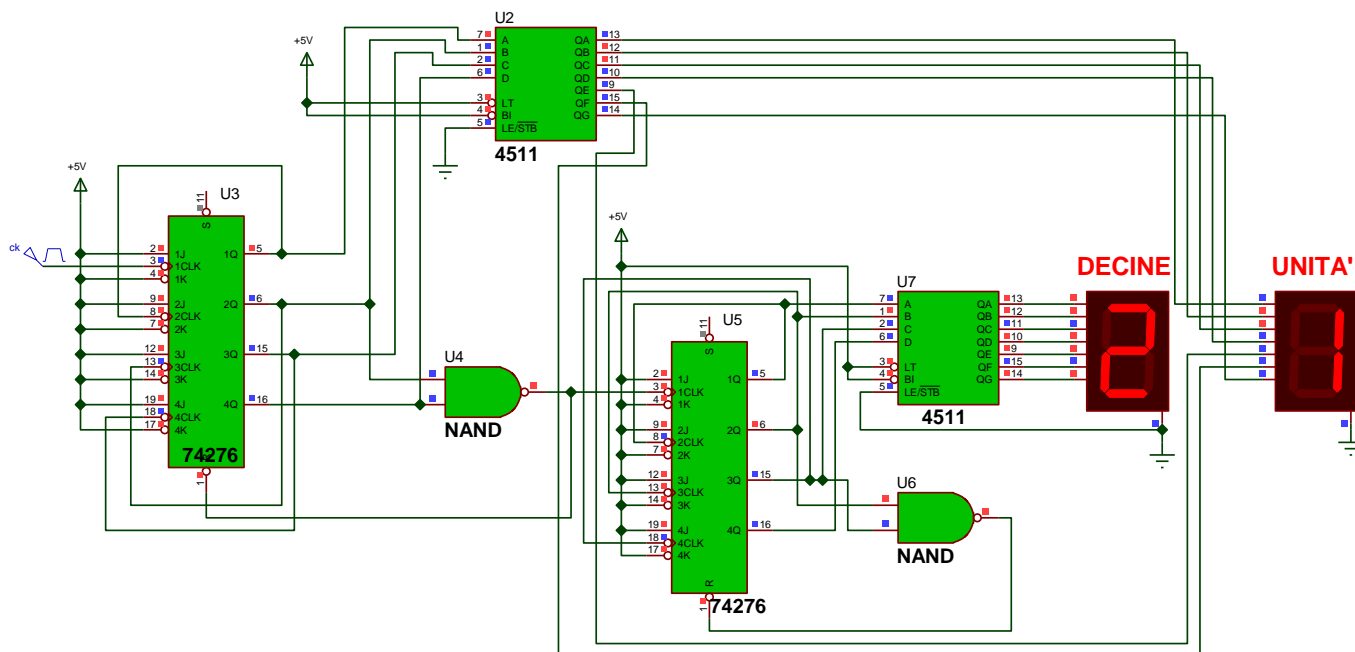
4. FUNZIONE LOGICA A 4 VARIABILI



5. CONTATORE-DECODER-DISPLAY mod 16



6. Counter - decoder - display a 2 cifre dec



7. Accensione 10 led in sequenza

