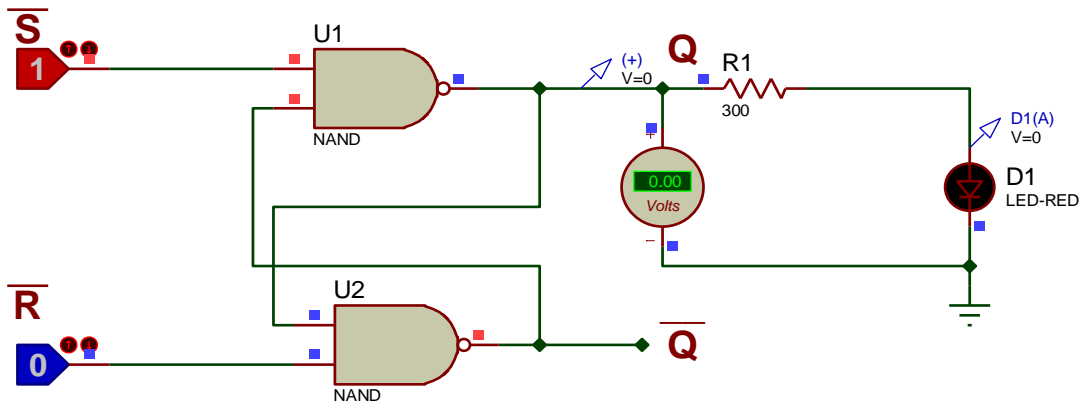


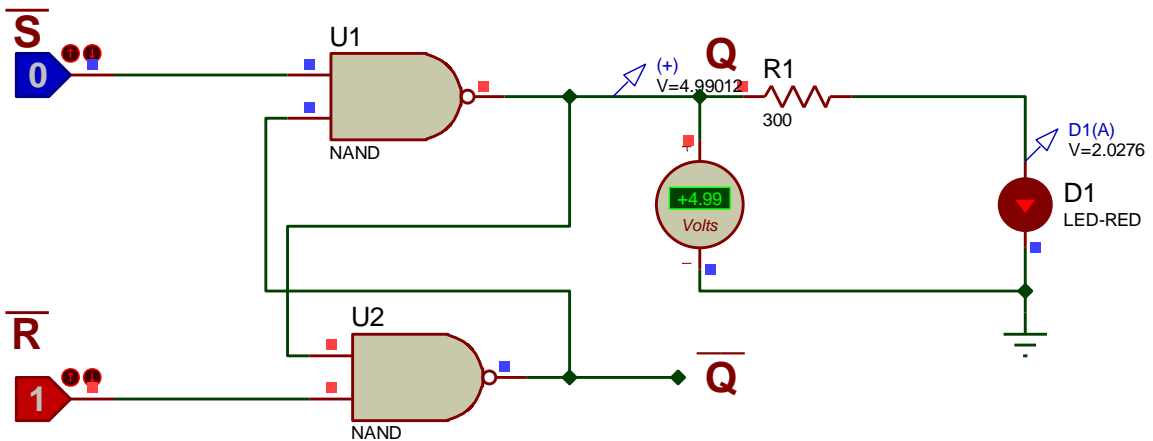
LOGICA SEQUENZIALE

1)

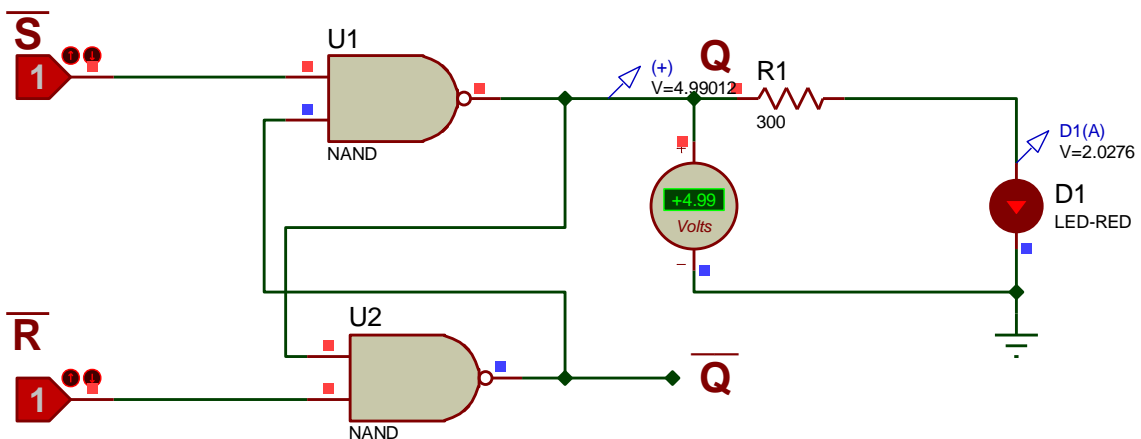
LATCH \overline{SR} (ATTIVO BASSO)



Attivazione del RESET : $Q = 0 \gggg V_Q = 0 [V] \gggg$ LED SPENTO



Attivazione del SET : $Q = 1 \gggg V_Q = 5 [V] \gggg$ LED ACCESO



Attivazione di MEM (dopo SET) : $Q_i = Q_{i-1} = 1 \gggg V_Q = 5 [V] \gggg$ LED ACCESO

Sn	Rn	Qi
0	0	N.A.
0	1	1 (SET)
1	0	0 (RES)
1	1	Qi-1 (MEM)

TAVOLA DI VERITA' LATCH SR att. Basso

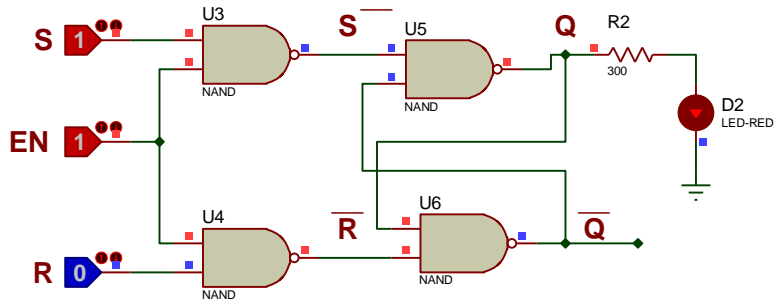
2)

LATCH SR CON ENABLE (ATTIVO ALTO)

a) **ENABLE ATTIVO**

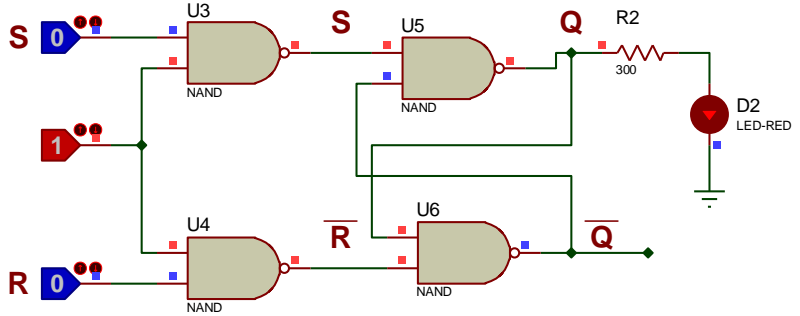
SET >>> Q = 1

LED ACCESO



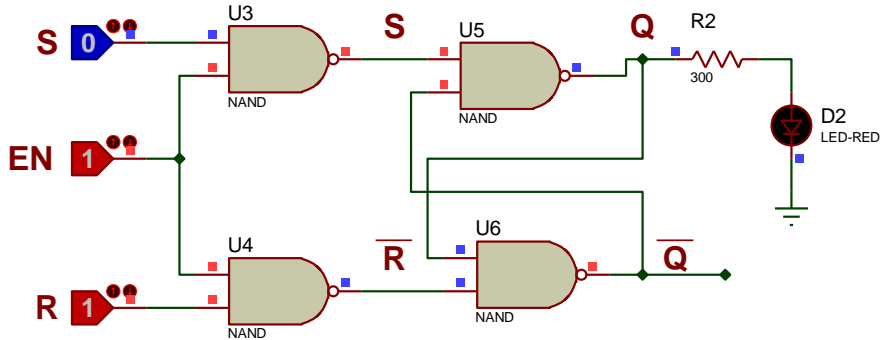
b) MEM (dopo SET) >>> Q = 1

LED ACCESO



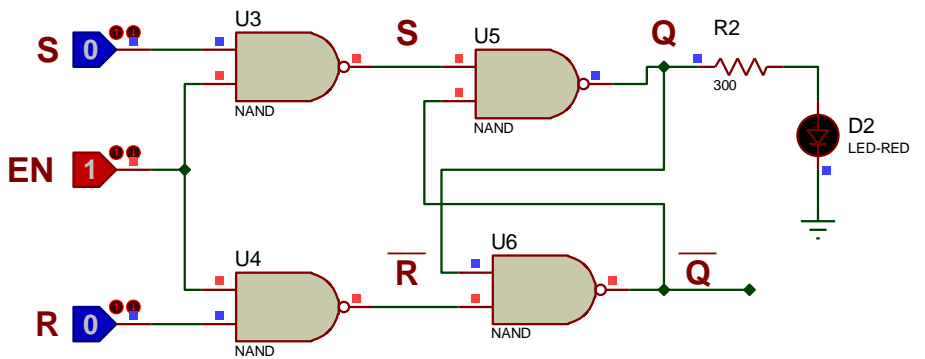
c) RES >>> Q = 0

LED SPENTO



d) MEM (dopo RESET) >>> Q = 0

LED SPENTO



e) **ENABLE DISATTIVO** : NON VIENE ESEGUITO ALCUN COMANDO

TAVOLA DI VERITA' LATCH SR con EN

ENABLE	S	R	Qi
0	X	X	Qi-1 (DIS)
1	0	0	Qi-1 (MEM)
1	0	1	0 (RESET)
1	1	0	1 (SET)
1	1	1	N.A.

3)

LATCH JK CON ENABLE (ATTIVO ALTO)

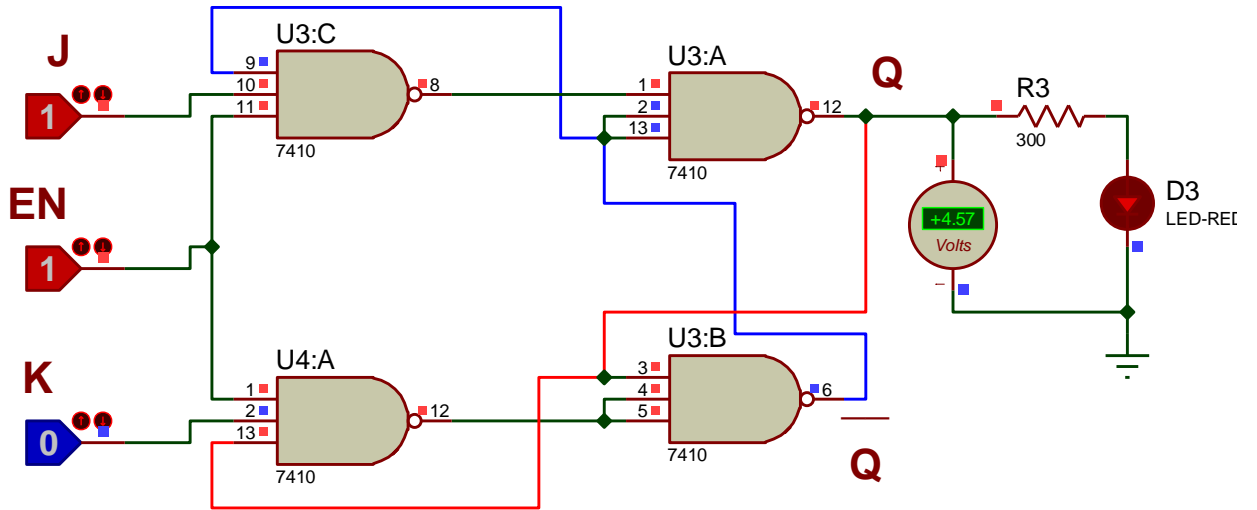
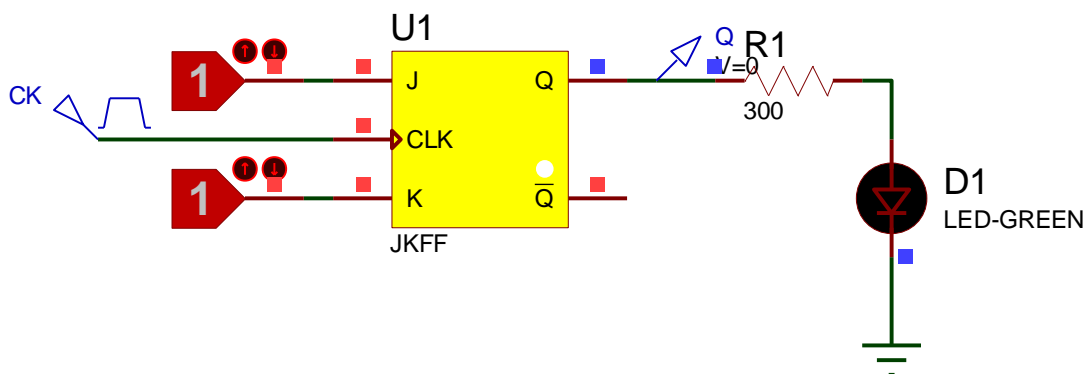


TAVOLA DI VERITA' LATCH JK con EN

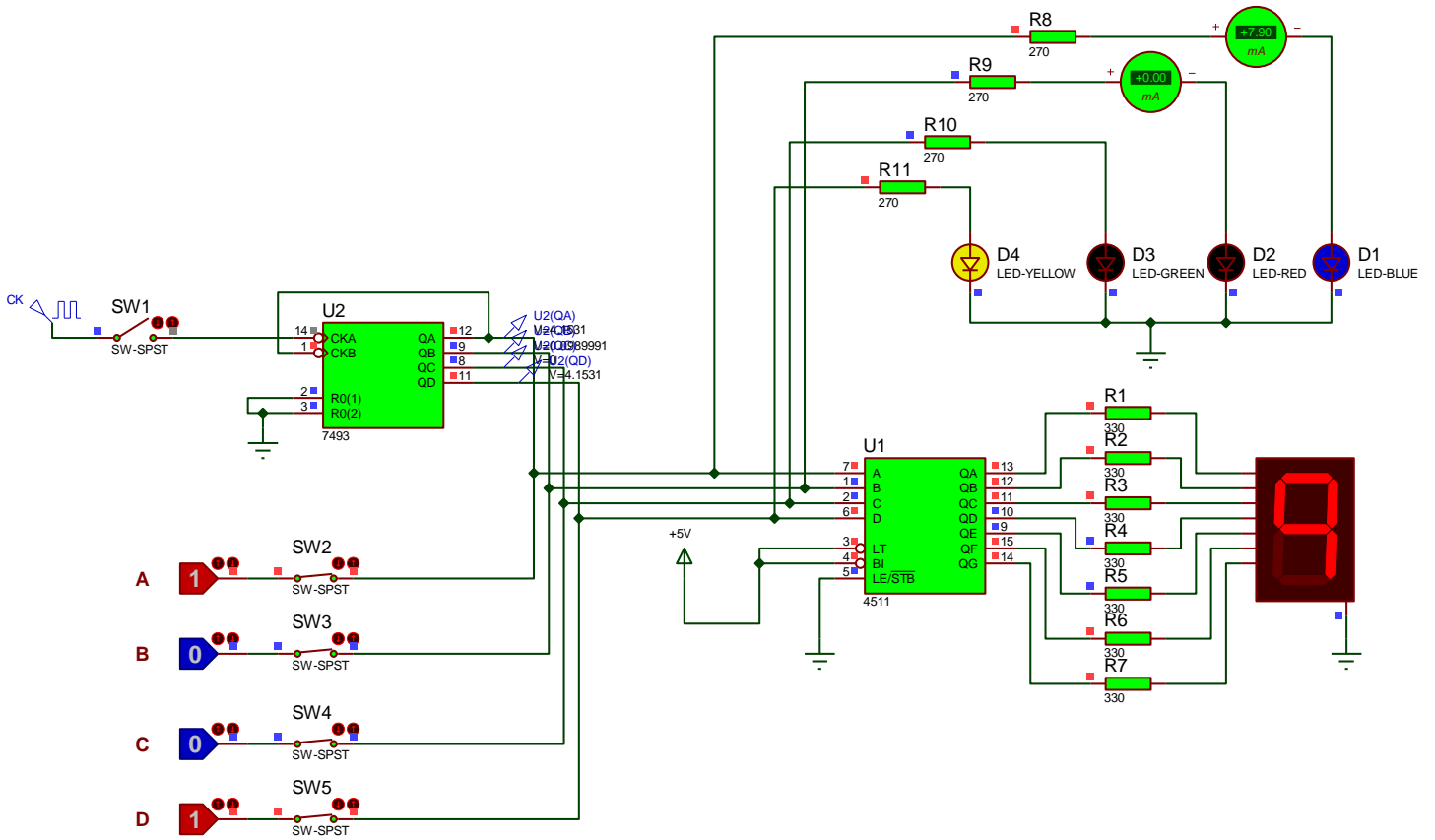
ENABLE	J	K	Qi
0	X	X	Qi-1 (DIS)
1	0	0	Qi-1 (MEM)
1	0	1	0 (RESET)
1	1	0	1 (SET)
1	1	1	Qi-1 negato (TOGGLE)

4)

FLIP FLOP JK

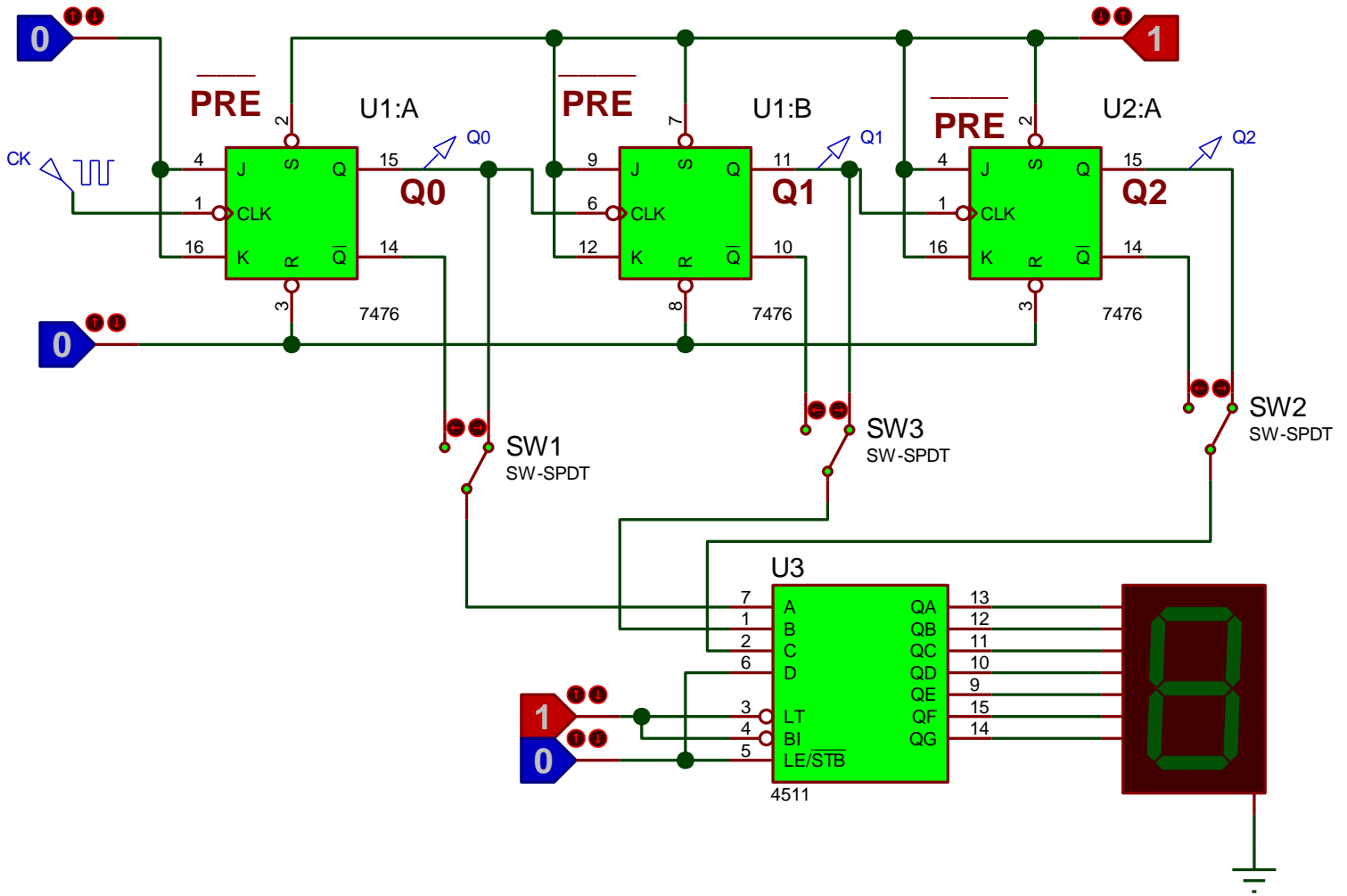


5) COUNTER 7493 + DECODER/DRIVER 4511 + DISPLAY a 7 seg. LED



6)

CONTATORE ASINCRONO UP / DOWN MODULO 8



7)

CONTATORE ASINCRONO UP / DOWN MODULO 6

