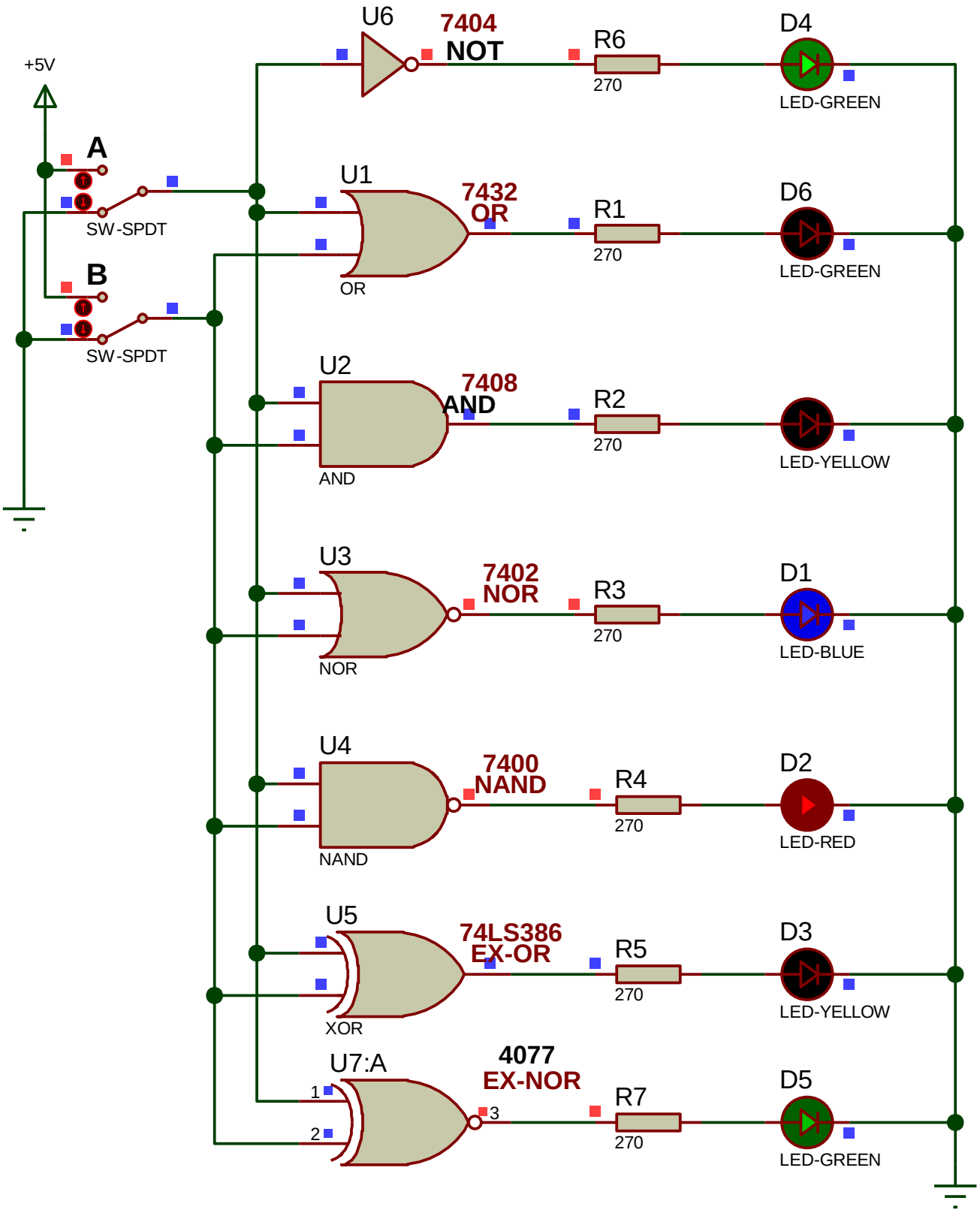
VERIFICA TAVOLE DI VERITA' PORTE LOGICHE ELEMENTARI

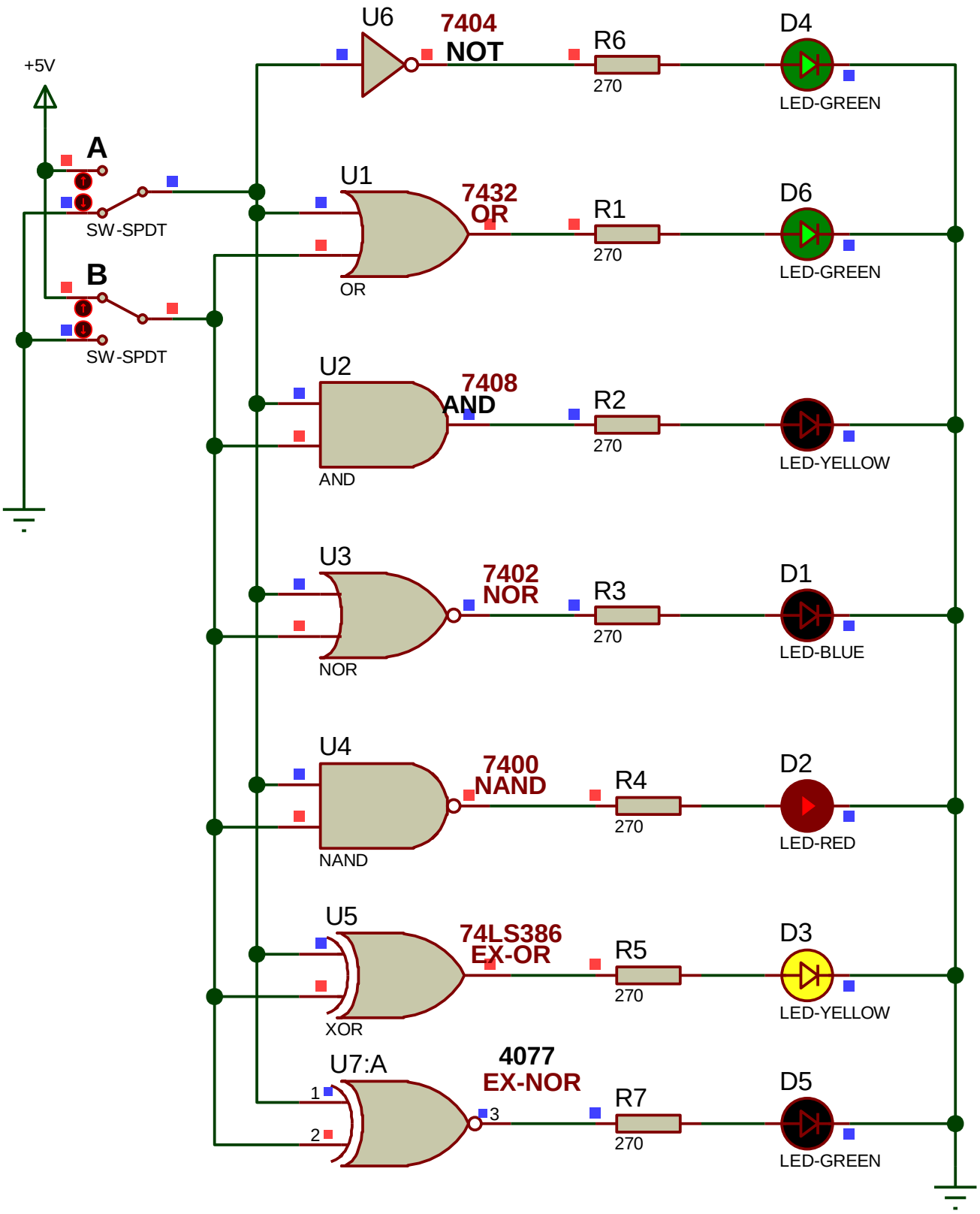
A	NOT $Y = \overline{A}$
0	1
1	0

A	B	OR Y =A+B	NOR $Y= \overline{A+B}$	AND Y=A*B	NAND $Y= \overline{A*B}$	EX-OR $Y=\overline{A}B+A\overline{B}$	EX-NOR $Y=\overline{AB}+AB$
0	0	0	1	0	1	0	1
0	1	1	0	0	1	1	0
1	0	1	0	0	1	1	0
1	1	1	0	1	0	0	1

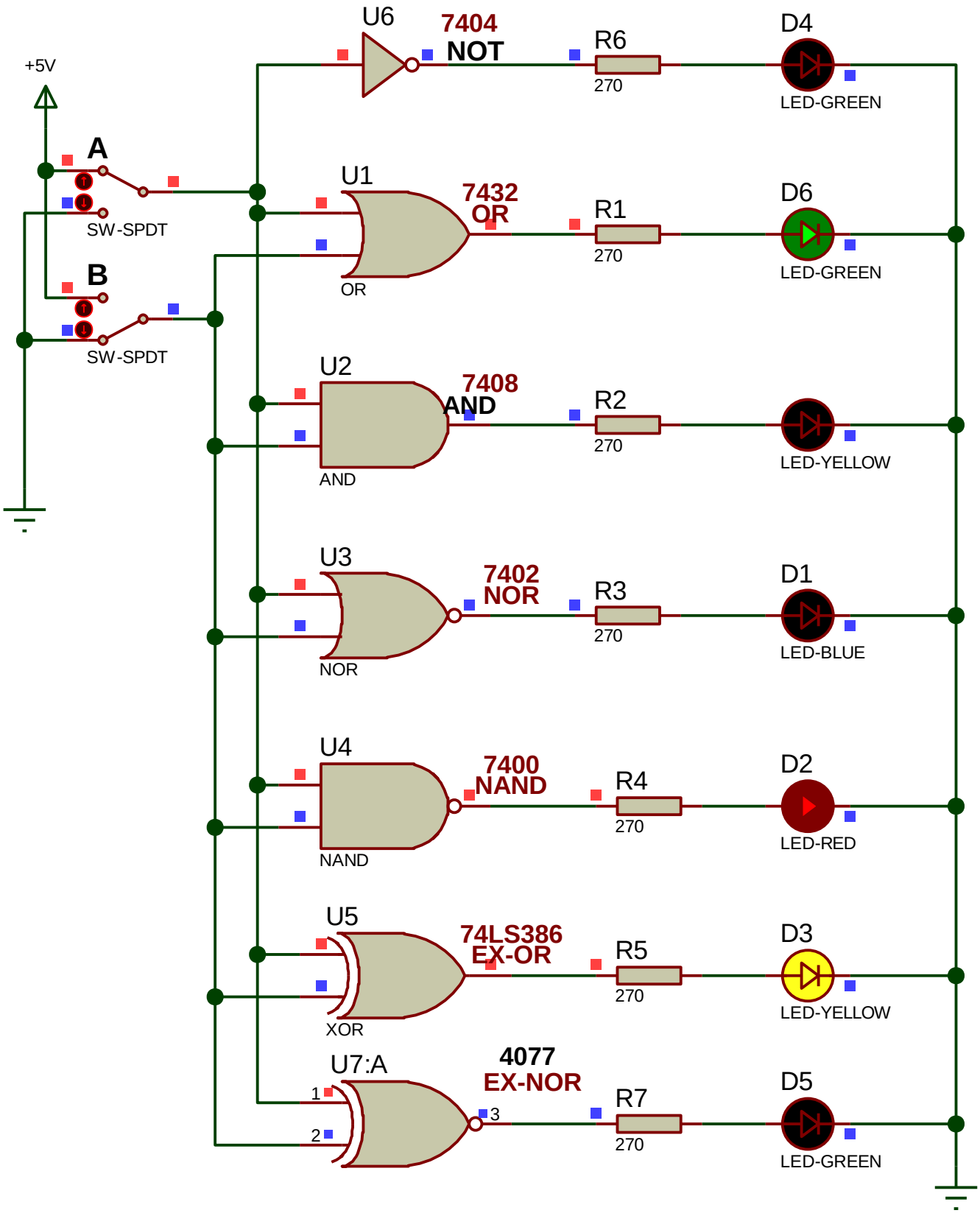
1° CASO : A B = 0 0


+5V
A
SW-SPDT
B
SW-SPDT
U6
7404
NOT
R6
270
D4
LED-GREEN
U1
7432
OR
OR
R1
270
D6
LED-GREEN
U2
7408
AND
AND
R2
270
LED-YELLOW
U3
7402
NOR
NOR
R3
270
D1
LED-BLUE
U4
7400
NAND
NAND
R4
270
D2
LED-RED
U5
74LS386
EX-OR
XOR
R5
270
D3
LED-YELLOW
U7:A
4077
EX-NOR
1
2
3
R7
270
D5
LED-GREEN

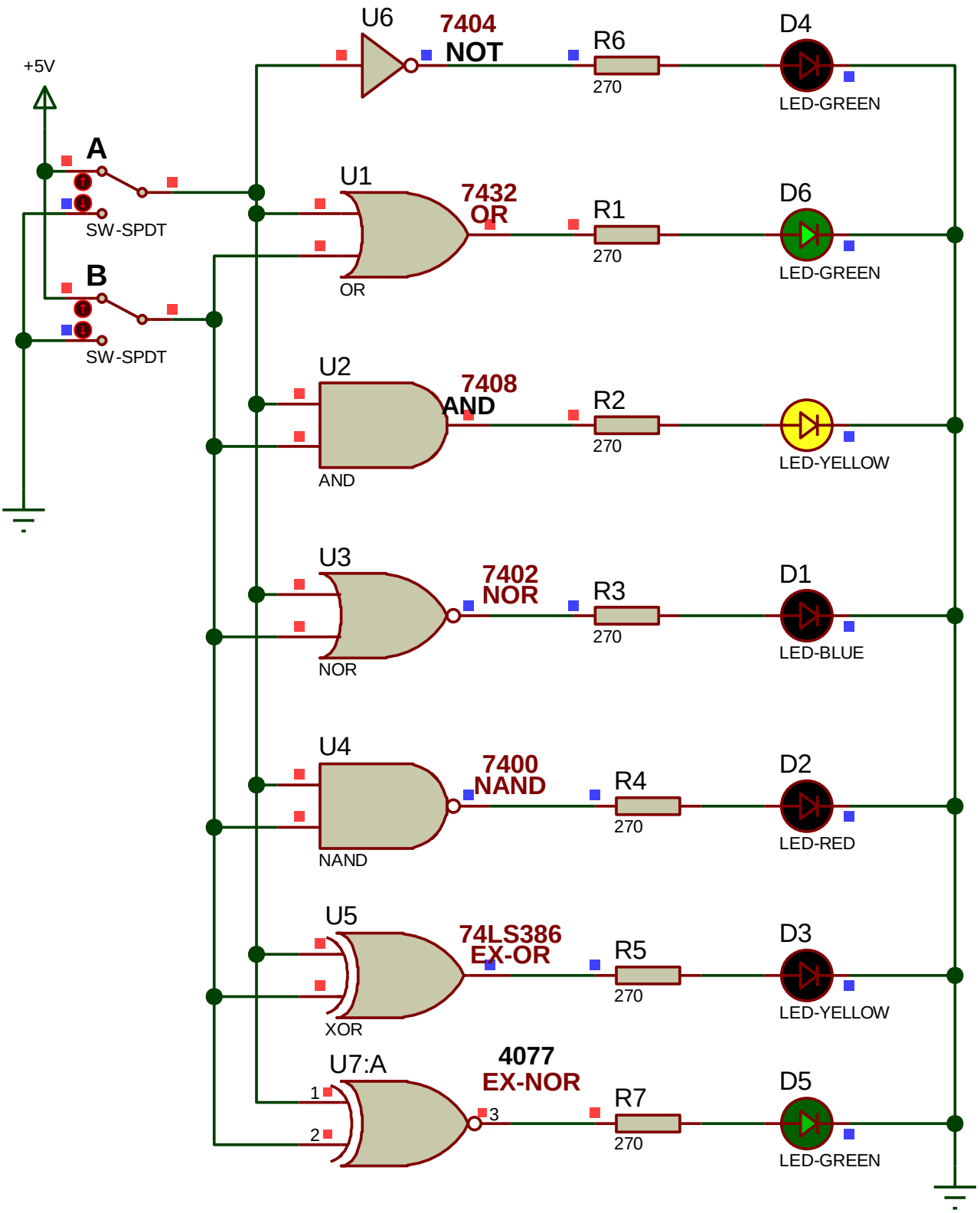
2° CASO : A B = 0 1


+5V
A
SW-SPDT
B
SW-SPDT
U6
7404
NOT
R6
270
D4
LED-GREEN
U1
7432
OR
OR
R1
270
D6
LED-GREEN
U2
7408
AND
AND
R2
270
LED-YELLOW
U3
7402
NOR
NOR
R3
270
D1
LED-BLUE
U4
7400
NAND
NAND
R4
270
D2
LED-RED
U5
74LS386
EX-OR
XOR
R5
270
D3
LED-YELLOW
U7:A
4077
EX-NOR
1
2
3
R7
270
D5
LED-GREEN

3° CASO : A B = 1 0


+5V
A
SW-SPDT
B
SW-SPDT
U6
7404
NOT
R6
270
D4
LED-GREEN
U1
7432
OR
OR
R1
270
D6
LED-GREEN
U2
7408
AND
AND
R2
270
LED-YELLOW
U3
7402
NOR
NOR
R3
270
D1
LED-BLUE
U4
7400
NAND
NAND
R4
270
D2
LED-RED
U5
74LS386
EX-OR
XOR
R5
270
D3
LED-YELLOW
U7:A
4077
EX-NOR
1
2
3
R7
270
D5
LED-GREEN

4° CASO : A B = 1 1


+5V
A
SW-SPDT
B
SW-SPDT
U6
7404
NOT
R6
270
D4
LED-GREEN
U1
7432
OR
OR
R1
270
D6
LED-GREEN
U2
7408
AND
AND
R2
270
LED-YELLOW
U3
7402
NOR
NOR
R3
270
D1
LED-BLUE
U4
7400
NAND
NAND
R4
270
D2
LED-RED
U5
74LS386
EX-OR
XOR
R5
270
D3
LED-YELLOW
U7:A
4077
EX-NOR
1
2
3
R7
270
D5
LED-GREEN